

**IN THE CLAIMS**

The text of all pending claims, including withdrawn claims, is set forth below. Cancelled and not entered claims are indicated with claim number and status only. The claims as listed below show added text with underlining and deleted text with ~~strikethrough~~. The status of each claim is indicated with one of (original), (currently amended), (cancelled), (withdrawn), (new), (previously presented), or (not entered). Please AMEND claims 1, 8, and 9 in accordance with the following:

1. (Currently Amended) A storage device for maintaining information, which is accessed by a host device through a host interface, when power is OFF and being capable of executing a test process based on test signals, comprising:

    a memory including a plurality of memory locations and storing secret data or initial data;  
    a test terminal inputting the test signals indicating a memory location among the plurality of memory locations;

    an instruction part sending a read out instruction for instructing the memory storing secret data to read out data stored at the memory location;

    a decoding part decoding ~~whether the data~~ read out by the memory stored at the memory location in response to the data reading instruction and determining whether the data is [[the ]] secret data or [[the ]]initial data;

    a maintaining part maintaining information ~~in a volatile state resulting from the decoding part in a volatile state~~; and

    a cutting-off part cutting off the test signals input from the test terminal when the maintaining part maintains information indicating that [[the ]]secret data is stored at the memory location.

2. (Original) The storage device as claimed in claim 1, wherein said read out instruction sent by said instruction part is a secret data read out instruction for instructing the memory storing secret data to read out the secret data.

3. (Original) The storage device as claimed in claim 1, wherein said read out instruction sent by said instruction part is a data read out instruction for instructing the memory storing secret data to read out all data stored in the memory other than working data.

4. (Original) The storage device as claimed in claim 1, wherein said read out instruction sent by said instruction part is a data read out instruction for instructing the memory storing secret data to read out data indicating a presence of the secret data stored in an area that is not for the secret data.

5. (Original) The storage device as claimed in claim 1, wherein said instruction part sends the read out instruction when the power is ON.

6. (Original) The storage device as claimed in claim 1, wherein said instruction part sends the read out instruction when the memory is reset.

7. (Original) The storage device as claimed in claim 1, wherein said instruction part sends the read out instruction when a command for operating secret data is made.

8. (Currently Amended) A storage device for maintaining information, which is accessed by a host device through a host interface, when the power is OFF and being capable of executing a test process based on test signals, comprising:

a memory including a plurality of memory locations and storing secret data or initial data;

a decoding part gathering a set of data read out by the memory storing secret data or initial data at a memory location among the plurality of memory locations in response to an access request indicating the memory location, and decoding the data read out by based on the set of data whether the secret data or the initial data is stored at the memory location;

a maintaining part maintaining information in a volatile state resulting from the decoding part in a volatile state; and

a cutting-off part cutting off the test signals input from a test terminal when the maintaining part maintains information indicating that [[the ]]secret data is stored at the memory location.

9. (Currently Amended) A storage device for maintaining information, which is accessed by a host device through a host interface, when power is OFF and being capable of executing a test process based on test signals, comprising:

a memory including a plurality of memory locations and storing secret data or initial data;

a maintaining part maintaining, in a volatile state, information indicating that an access request is conducted to a memory location among the plurality of memory locations storing

secret data; and

    a cutting-off part cutting off the test signals input from a test terminal when the maintaining part maintains [[the ]]information indicating that the access request is conducted to [[the ]]a memory location storing secret data.

10. (Previously Presented) A storage device for non-volatile storage of information and which executes a test process, the storage device communicating with a host via a host interface, the storage device comprising:

    a memory including a plurality of memory locations and storing secret data or initial data;  
    a test terminal which receives at least one test signal indicating a memory location among the plurality of memory locations from which to read out data;

    a maintaining part which maintains information about the data stored at the memory location in a volatile state;

    a cutting-off part which cuts off the at least one test signal from the test terminal when the maintaining part maintains information indicating that the data stored at the memory location includes secret data.